

Description

The DFI900HF17I4RE1N is a Half Bridge IGBT Power Module. It integrates high performance IGBT chips designed for the applications such as High Power supply and Motor control.



Features

- Blocking voltage:1700V
- Enhanced FRD
- Low saturation voltage $V_{CE(sat)}$
- Low Switching Losses
- 175°C maximum junction temperature
- Thermistor inside

Applications

- High Power Switching Applications
- Motor Drives
- Solar inverter Systems
- Wind Turbines

Circuit diagram

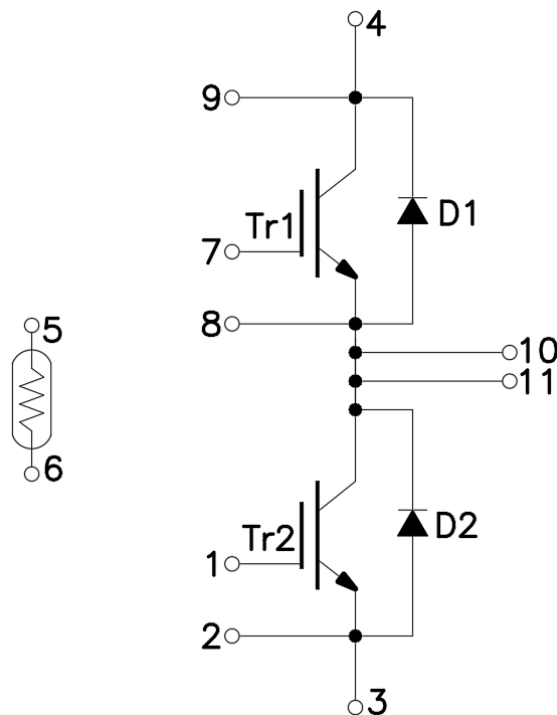


Figure 1. Out drawing & circuit diagram for DFI900HF17I4RE1N

Pin Configuration and Marking Information

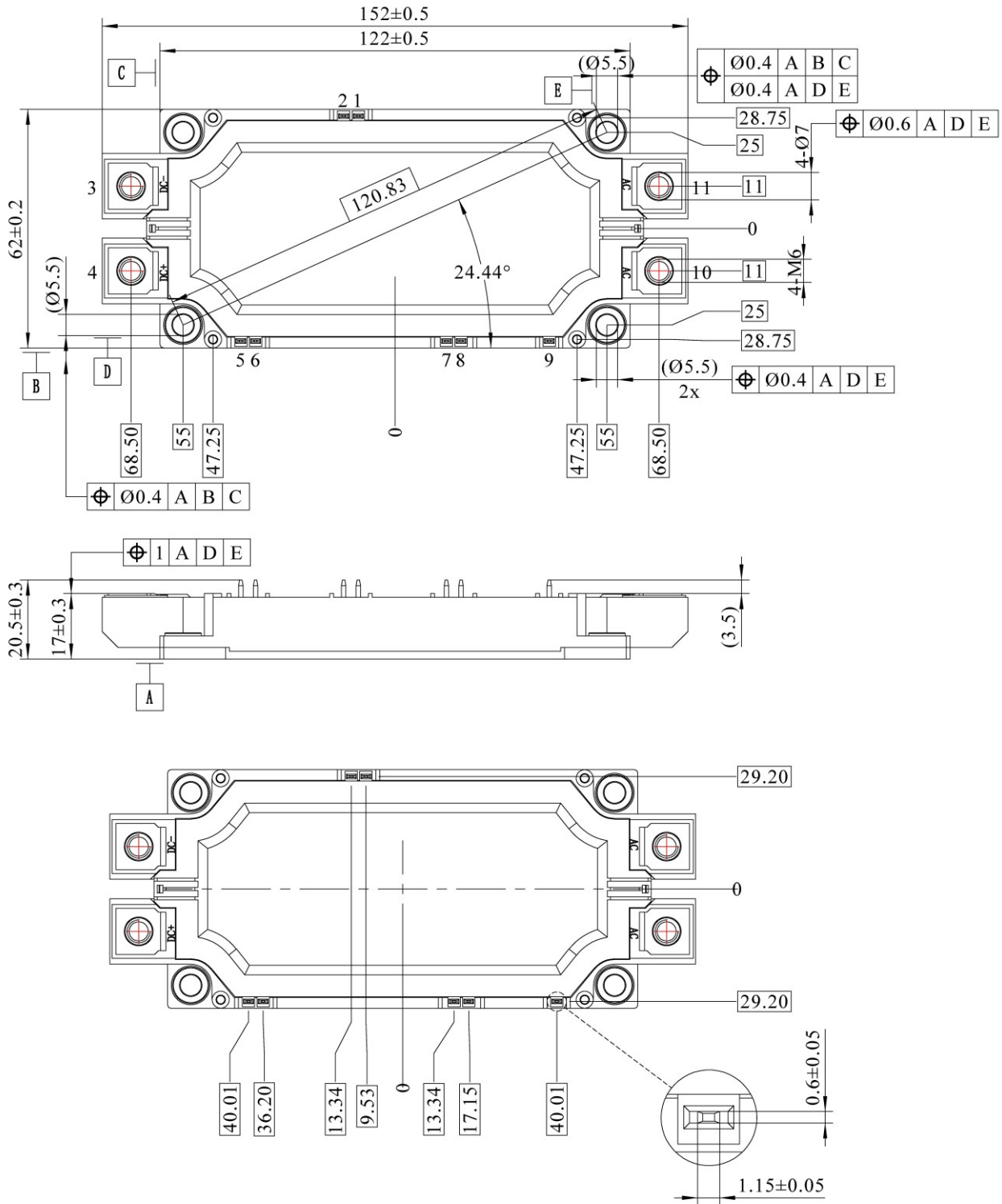


Figure 2. Pin configuration

Module

Parameter	Conditions	Value	Unit
Isolation Voltage	RMS, f=50Hz, t=1min	4.0	kV
Material of module baseplate	-	Cu	-
Creepage distance	terminal to heatsink terminal to terminal	14.5 13	mm
Clearance	terminal to heatsink terminal to terminal	12.5 10	mm
CTI	-	600	-
Module lead resistance, terminals–chip	T _C =25°C	0.8	mΩ
Mounting torque for module mounting	M5, M6	3 to 6	Nm
Weight	-	350	g

Maximum Ratings (T_j=25°C unless otherwise specified)

Symbol	Parameter	Conditions	Ratings	Unit
V _{CES}	Collector-Emitter Voltage	G-E Short	1700	V
V _{GES}	Gate-Emitter Voltage	C-E Short	±30	V
I _C	DC Continuous Collector Current	T _C =105°C	900	A
I _{CM}	Pulse Collector Current	t _p =1ms, Note1	1800	A
P _C	Maximum Power Dissipation	T _C =25°C	5769	W
I _F	Diode Forward Current	-	900	A
I _{FRM}	Repetitive peak forward Current	t _p =1ms, Note1	1800	A
T _j	junction temperature	-	-40 to 175	°C
T _{stg}	Storage temperature	-	-40 to 125	°C

Note1: Pulse width limited by maximum junction temperature

NTC characteristics

Symbol	Parameter	Condition	Value			Unit
			Min.	Typ.	Max.	
R ₂₅	Resistance	T _C =25°C	-	5	-	kΩ
ΔR/R	Deviation of R100	T _C =100°C, R ₁₀₀ =493Ω	-5	-	5	%
P ₂₅	Power dissipation	T _C =25°C	-	-	20	mW
B _{25/50}	B-value	R ₂ = R ₂₅ exp [B _{25/50} (1/T ₂ - 1/(298,15 K))]	-	3375	-	K
B _{25/80}	B-value	R ₂ = R ₂₅ exp [B _{25/80} (1/T ₂ - 1/(298,15 K))]	-	3411	-	K
B _{25/100}	B-value	R ₂ = R ₂₅ exp [B _{25/100} (1/T ₂ - 1/(298,15 K))]	-	3433	-	K

IGBT Electrical characteristics (T_j=25°C unless otherwise specified, chip)

Symbol	Item	Condition		Value			Unit
				Min.	Typ.	Max.	
V _{CE(sat)} (Chip)	Collector-Emitter Saturation Voltage	I _C =900A V _{GE} =15V	T _j =25°C	-	1.55	-	V
			T _j =150°C	-	1.90	-	V
			T _j =175°C	-	1.95	-	V
V _{GE(th)}	Gate-Emitter threshold Voltage	I _C =34mA, V _{CE} =10V	T _j =25°C	5.0	5.5	6.5	V
I _{CES}	Collector- Emitter Cut off Current	V _{CE} =1700V, V _{GE} =0V	T _j =25°C	-	-	1	mA
I _{GES}	Gate-Emitter Leakage Current	V _{GE} =±30V, V _{CE} =0V	T _j =25°C	-	-	2	uA
C _{ies}	Input Capacitance	V _{CE} =25V V _{GE} =0V f=1MHz	T _j =25°C	-	62.0	-	nF
C _{oes}	Output Capacitance		T _j =25°C	-	2.3	-	nF
C _{res}	Reverse transfer Capacitance		T _j =25°C	-	0.9	-	nF
Q _G	Gate charge	V _{GE} = -15V to +15V	T _j =25°C	-	7.8	-	uC
R _{Gint}	Internal gate resistor	-	T _j =25°C	-	1.3	-	Ω
t _{d(on)}	Turn-on delay time	V _{CC} =900V I _C =900A V _{GE} =+15V/-8V R _G =1.0Ω Inductive load	T _j =25°C	-	251	-	ns
			T _j =125°C	-	246	-	
			T _j =150°C	-	242	-	
t _r	Rise time		T _j =25°C	-	287	-	ns
			T _j =125°C	-	299	-	
			T _j =150°C	-	305	-	
t _{d(off)}	Turn-off delay time		T _j =25°C	-	1202	-	ns
			T _j =125°C	-	1508	-	
			T _j =150°C	-	1595	-	
t _f	Fall time		T _j =25°C	-	608	-	ns
			T _j =125°C	-	1131	-	
			T _j =150°C	-	1273	-	
E _{on}	Turn-on power dissipation		T _j =25°C	-	420.5	-	mJ
			T _j =125°C	-	532.2	-	
			T _j =150°C	-	566.8	-	
E _{off}	Turn-off power dissipation	T _j =25°C	-	304.9	-	mJ	
		T _j =125°C	-	387.2	-		
		T _j =150°C	-	408.3	-		
R _{th(j-c)}	Thermal Resistance, Junction to Case (IGBT)			-	0.026	-	K/W
R _{th(c-s)}	Thermal Resistance, Case to sink (Conductive Grease applied) , Note1			-	0.015	-	K/W

Note1: Assumes Thermal Conductivity of grease is 2.8 W/m · K and thickness is 50um

Freewheeling Diode Electrical characteristics (T_j=25°C unless otherwise specified, chip)

Symbol	Item	Condition	Value			Unit	
			Min.	Typ.	Max.		
V _F (Chip)	Diode Forward Voltage	I _F =900A, V _{GE} =0V	T _j =25°C	-	2.00	-	V
			T _j =150°C	-	2.03	-	
			T _j =175°C	-	1.87	-	
t _{rr}	Reverse recovery time	(Switch side) V _{CC} =900V, I _C =900A V _{GE} =+15V/-8V R _G =1.0Ω	T _j =25°C	-	913	-	us
			T _j =125°C	-	1198	-	
			T _j =150°C	-	1329	-	
I _{RM}	Peak reverse recovery Current	(FRD side) V _{rr} =900V, I _F =900A V _{GE} =+15V/-8V Inductive load switching operation	T _j =25°C	-	249	-	A
			T _j =125°C	-	324	-	
			T _j =150°C	-	343	-	
Q _{rr}	Recovered charge	Inductive load switching operation	T _j =25°C	-	99	-	uC
			T _j =125°C	-	193	-	
			T _j =150°C	-	225	-	
E _{rr}	Reverse recovered energy	Inductive load switching operation	T _j =25°C	-	50.6	-	mJ
			T _j =125°C	-	109.5	-	
			T _j =150°C	-	129.2	-	
R _{th(j-c)}	Thermal Resistance, Junction to Case (Diode)		-	0.038	-	K/W	
R _{th(c-s)}	Thermal Resistance, Case to sink (Conductive Grease applied), Note1		-	0.020	-	K/W	

Note1: Assumes Thermal Conductivity of grease is 2.8 W/m · K and thickness is 50um

Test Conditions

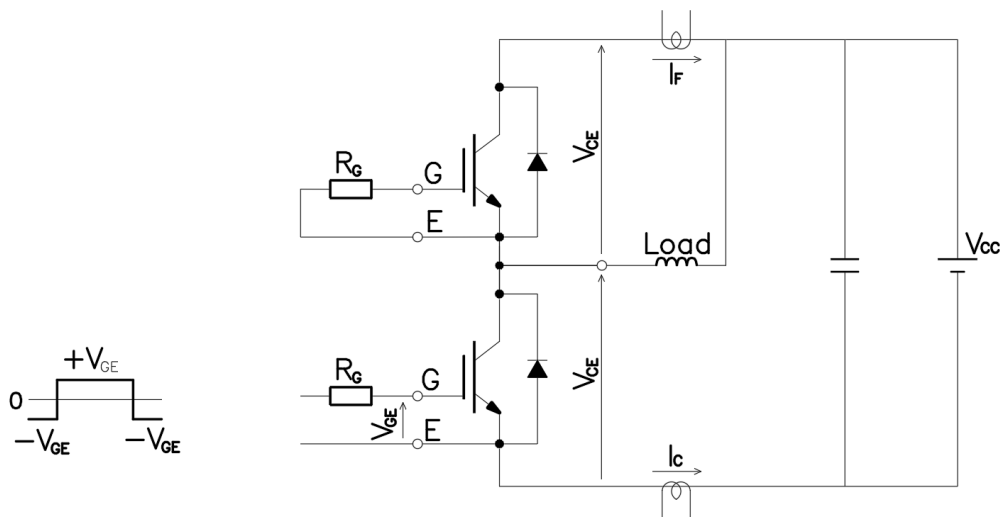


Figure 3. Switching time measure circuit

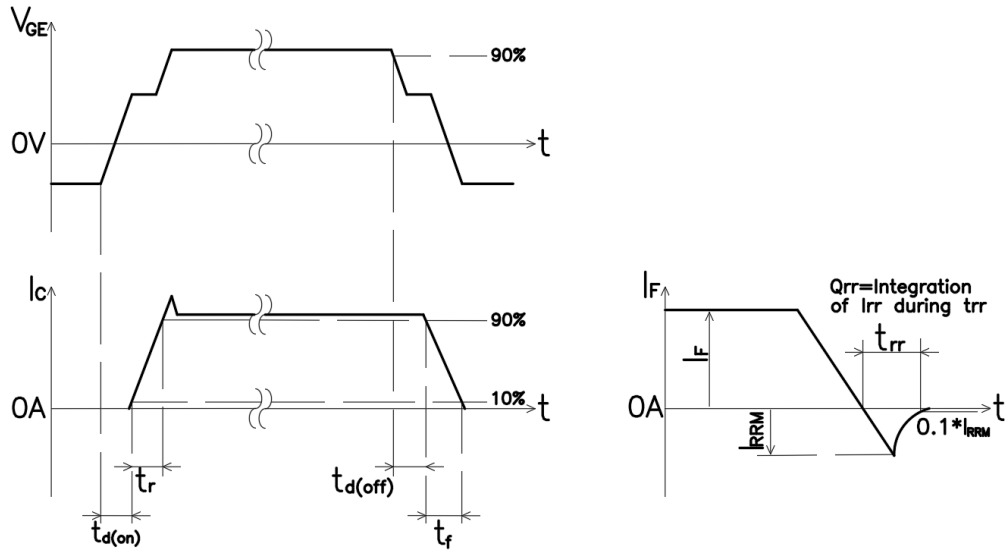


Figure 4. Switching time definition

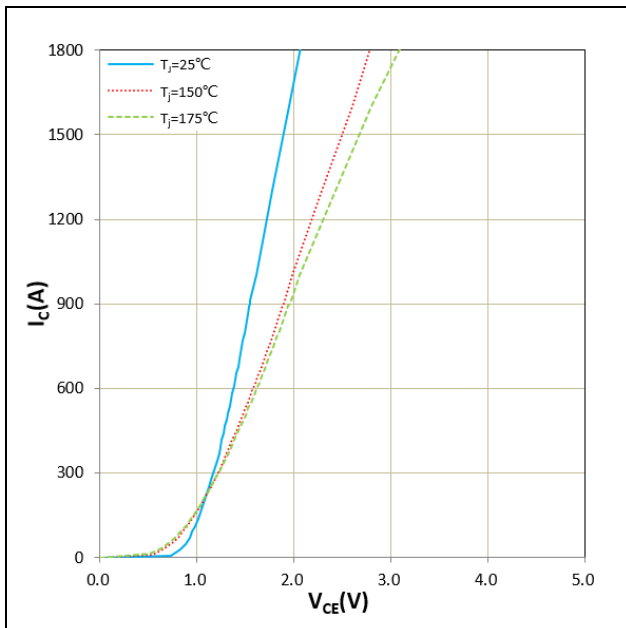


Figure 5. I_c vs V_{CE}
 $V_{GE} = 15V$

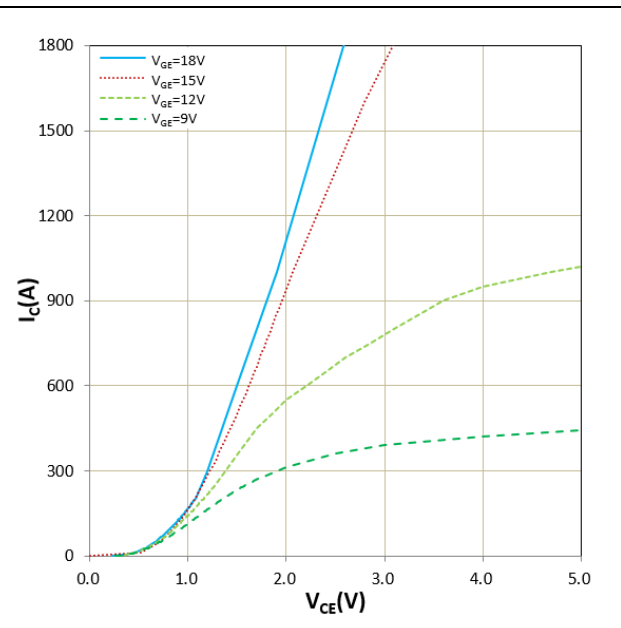


Figure 6. I_c vs V_{CE}
 $T_j = 175^\circ C$

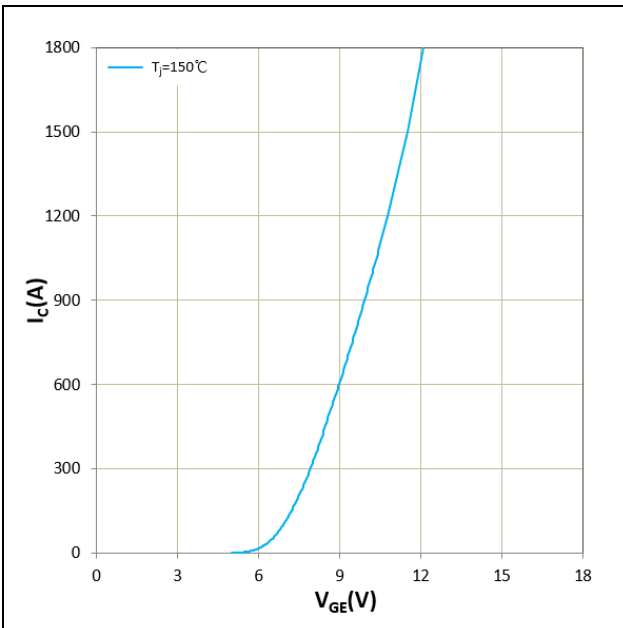


Figure 7. I_c vs V_{GE}
 $V_{CE}=20V$

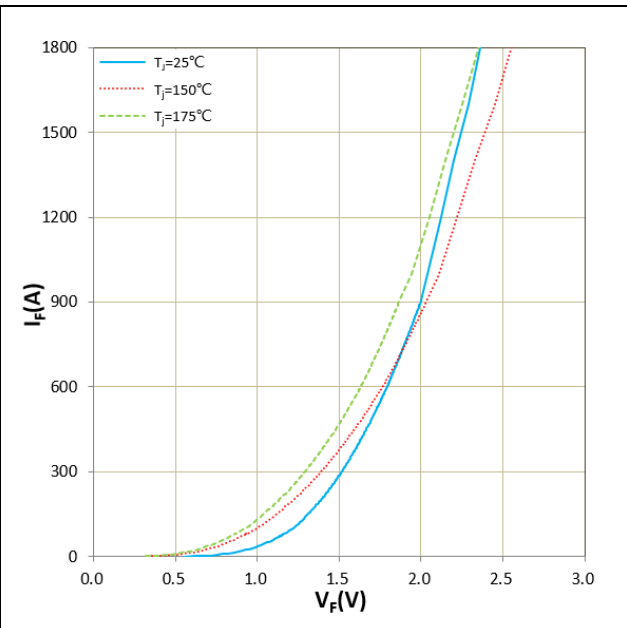


Figure 8. I_F vs V_F

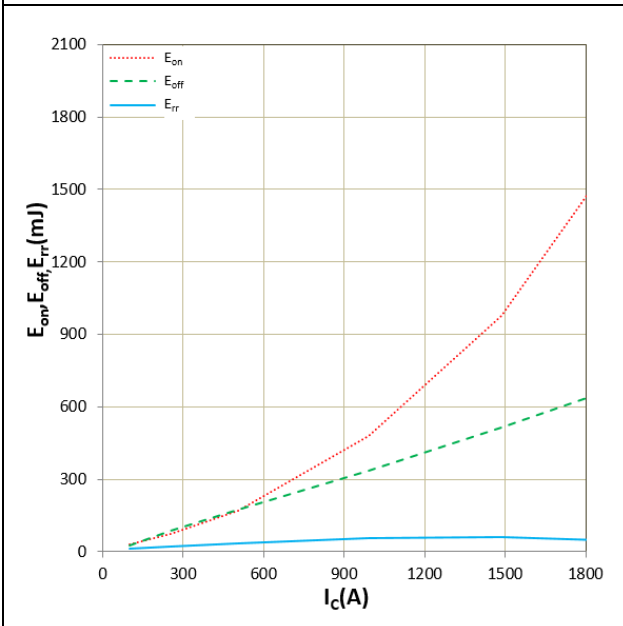


Figure 9. E_{on} , E_{off} , E_{rr} vs I_c (Typ)
 $V_{CC}=900V$, $V_{GE}=+15V/-8V$, $R_G=1.0\Omega$
 $T_j=25^\circ C$, Inductive Load

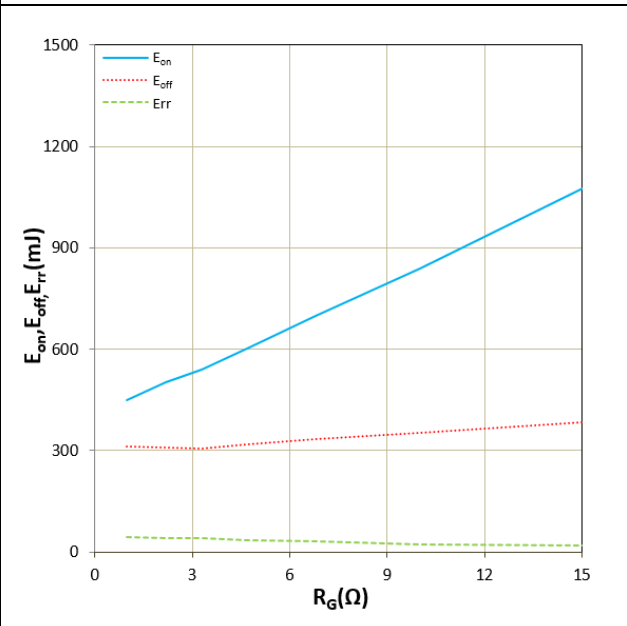


Figure 10. E_{on} , E_{off} , E_{rr} vs R_G (Typ)
 $V_{CC}=900V$, $V_{GE}=+15V/-8V$, $I_c=900A$
 $T_j=25^\circ C$, Inductive Load

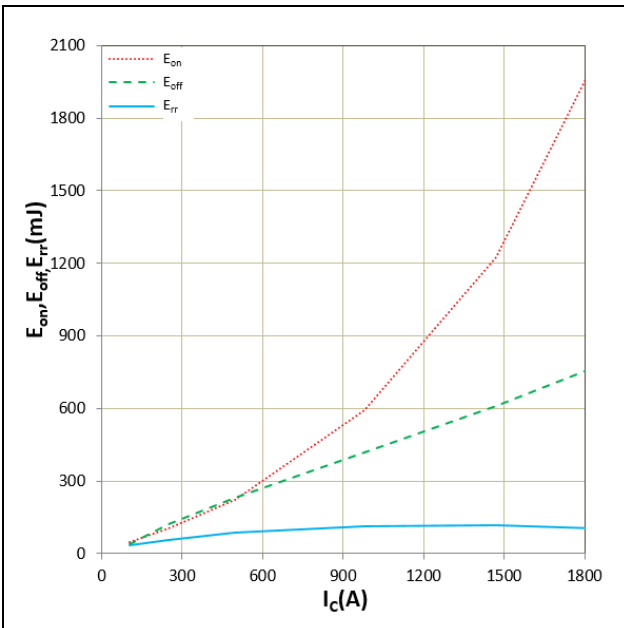


Figure 11. E_{on} , E_{off} vs I_c (Typ)
 $V_{CC}=900V$, $V_{GE}=+15V/-8V$, $R_G=1.0\Omega$
 $T_j=125^\circ C$, inductive Load

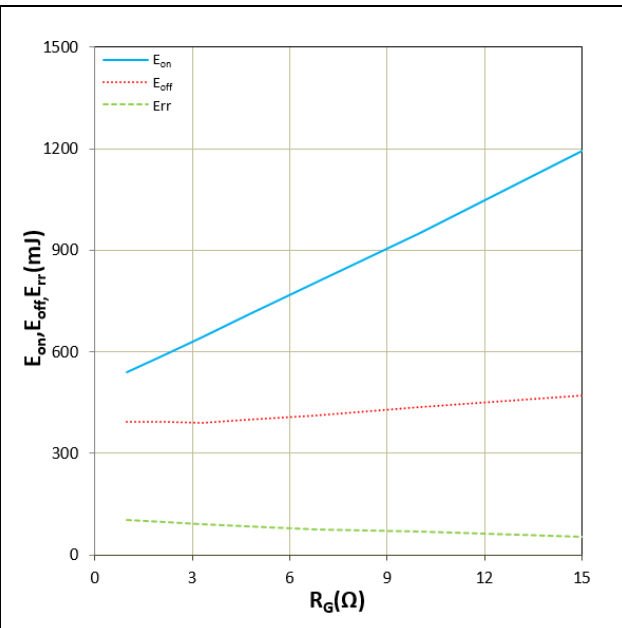


Figure 12. E_{on} , E_{off} , Err vs R_G (Typ)
 $V_{CC}=900V$, $V_{GE}=+15V/-8V$, $I_c=900A$
 $T_j=125^\circ C$, Inductive Load

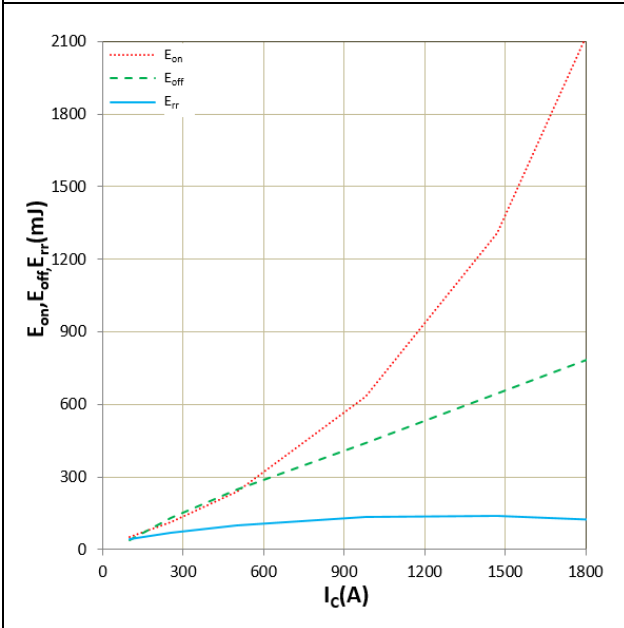


Figure 13. E_{on} , E_{off} vs I_c (Typ)
 $V_{CC}=900V$, $V_{GE}=+15V/-8V$, $R_G=1.0\Omega$
 $T_j=150^\circ C$, inductive Load

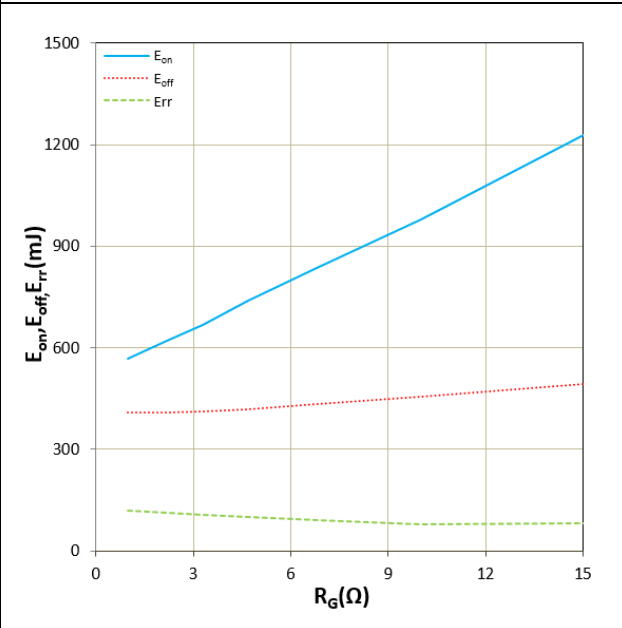


Figure 14. E_{on} , E_{off} , Err vs R_G (Typ)
 $V_{CC}=900V$, $V_{GE}=+15V/-8V$, $I_c=900A$
 $T_j=150^\circ C$, Inductive Load

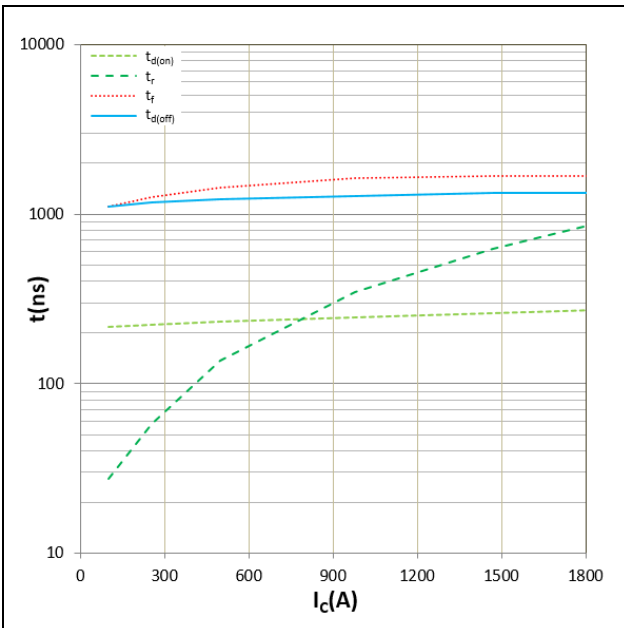


Figure 15. Switching time vs I_c (Typ)
 $V_{CC}=900V$, $V_{GE}=+15V/-8V$, $R_G=1.0\Omega$
 $T_j=150^\circ C$, Inductive Load

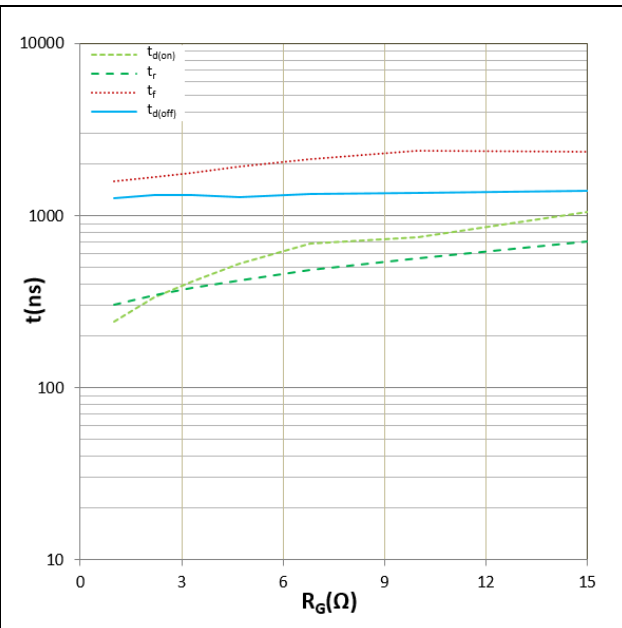


Figure 16. Switching time vs R_G (Typ)
 $V_{CC}=900V$, $V_{GE}=+15V/-8V$, $I_c=900A$
 $T_j=150^\circ C$, Inductive Load

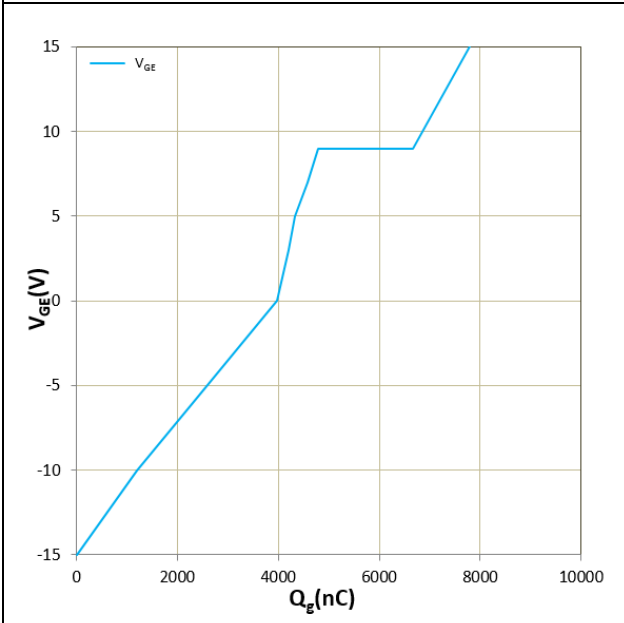


Figure 17. Gate charge

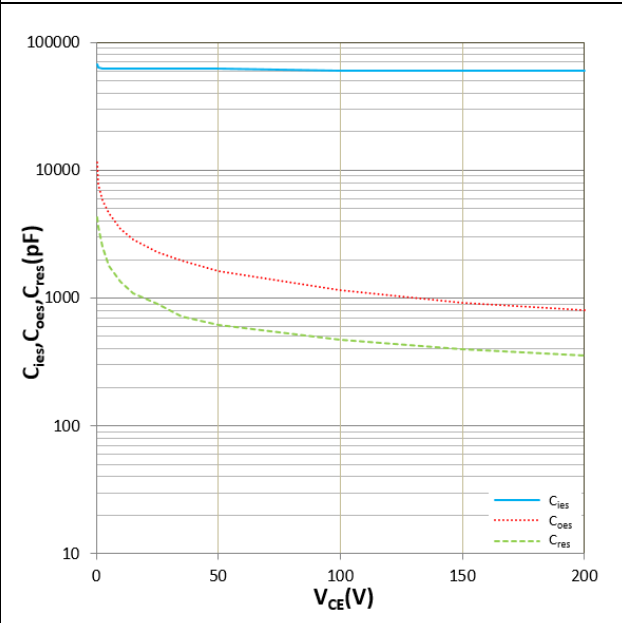
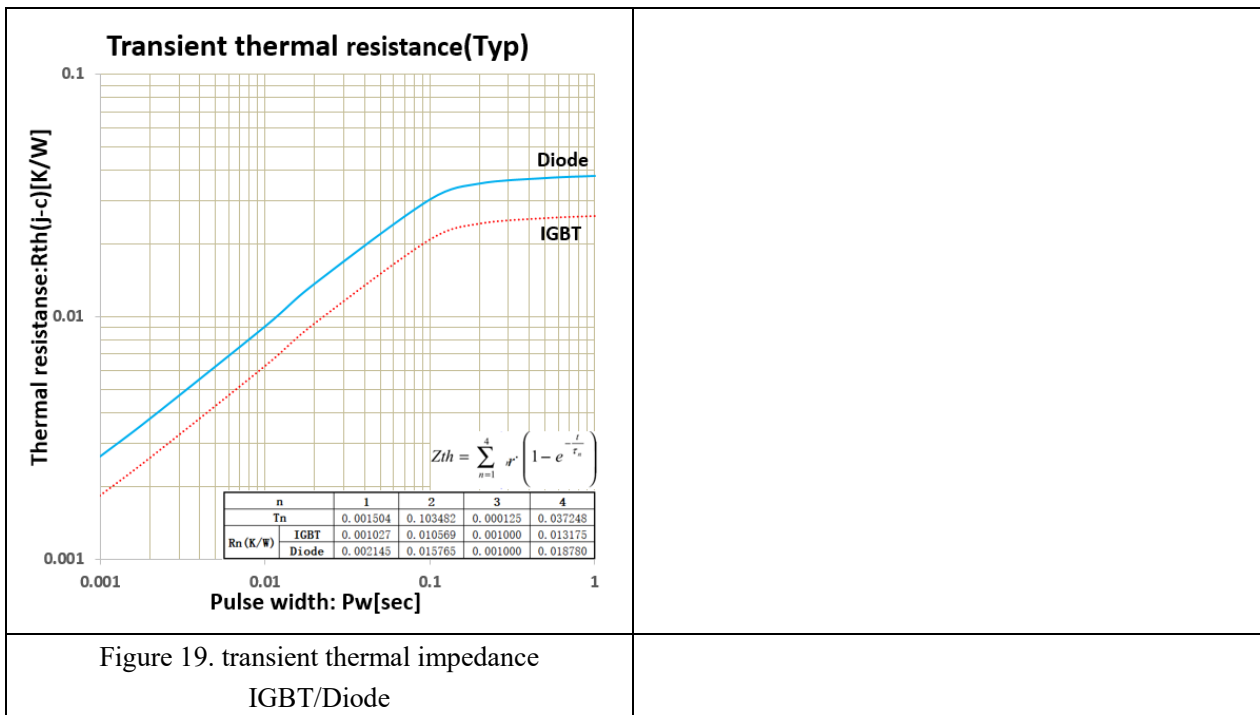


Figure 18. C_{ies} , C_{oes} , C_{res} vs V_{CE}
 $T_j=25^\circ C$, $f=1MHz$



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