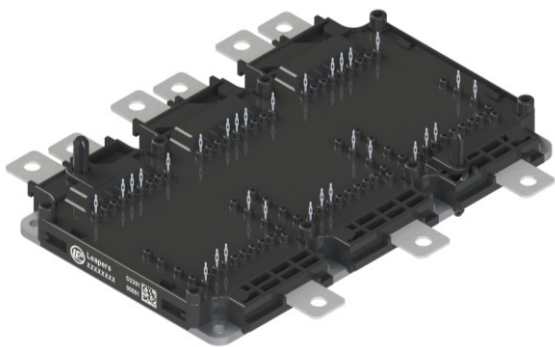


Description

The DFS03FB12HDB1 is a 3 Phase SiC MOSFET Power Module. It integrates high performance SiC MOSFET chips for xEV or motor drives application.



Features

- Blocking voltage 1200V
- $R_{DS(on)} = 2.2m\Omega$ ($T_j = 25^\circ C$)
- Arcbonding™ technology
- 175°C maximum junction temperature
- Si₃N₄ AMB substrate
- Direct Cooled Pin Fin Base Plate
- Thermistor inside
- Press FIT Contact Technology

Applications

- xEV Applications
- Motor Drives

Circuit diagram

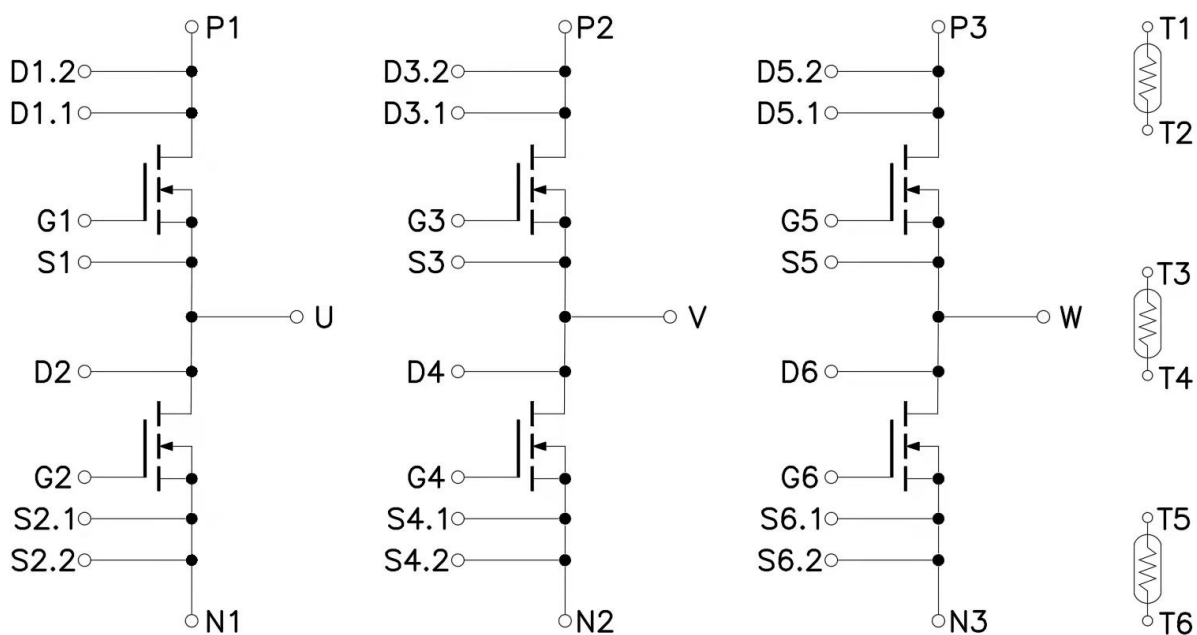


Figure 1. Out drawing & circuit diagram for DFS03FB12HDB1

Physical Dimensions

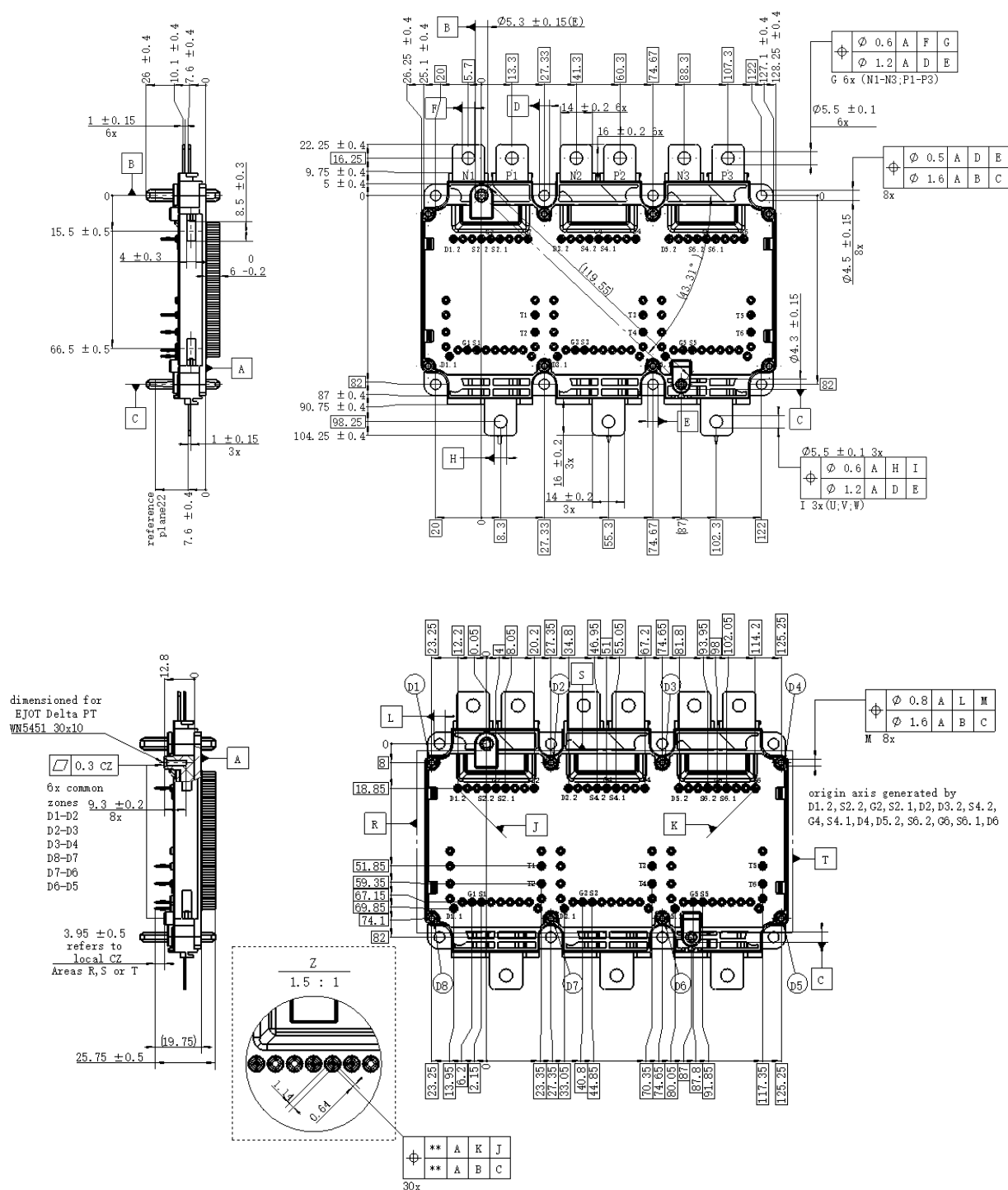


Figure2. Physical Dimensions

Maximum Ratings ($T_j = 25^\circ\text{C}$ unless otherwise specified)

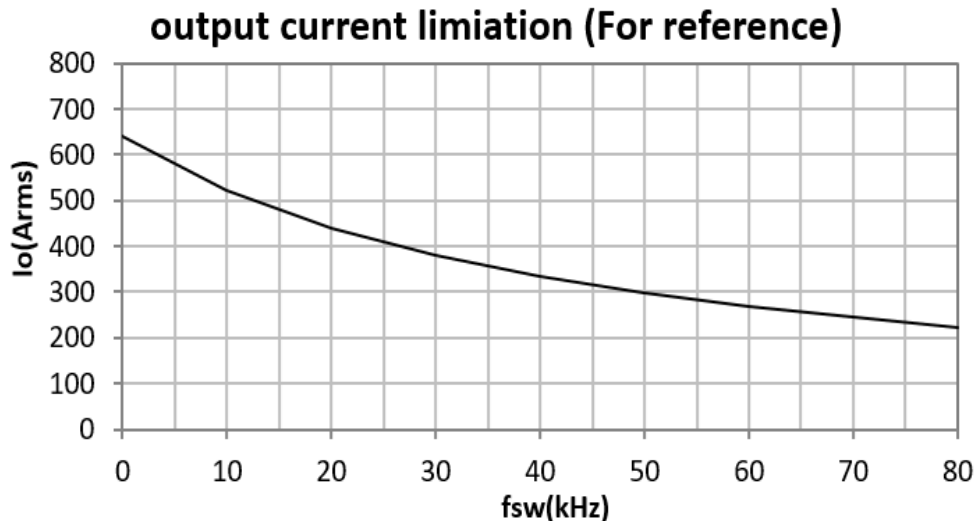
Symbol	Parameter	Conditions	Ratings	Unit
V_{DS}	Drain-Source Voltage	G-S Short	1200	V
V_{GS}	Gate-Source Voltage	D-S Short, AC frequency $\geq 1\text{Hz}$, Note1	-11V/+23V	V
I_{DS}	DC Continuous Drain Current	$T_f = 25^\circ\text{C}$	515	A
I_{DS}	DC Continuous Drain Current	$T_f = 65^\circ\text{C}$	440	A
I_{SD}	Source (Body Diode) Current	$T_f = 25^\circ\text{C}$, with ON signal	515	A
I_{SD}	Source (Body Diode) Current	$T_f = 65^\circ\text{C}$, with ON signal	440	A
I_{DP}	Drain Pulse Current, Peak	Less than 1ms, Note2	1200	A
P_D	Maximum Power Dissipation	$T_f = 25^\circ\text{C}$	1442	W
T_j	junction temperature	-	-40 to 175	$^\circ\text{C}$
T_{stg}	Storage temperature	-	-40 to 125	$^\circ\text{C}$

Note1: Recommended Operating Value: -4V/+15V, -5V/+18V

Note2: Pulse width limited by maximum junction temperature

Typical current output ability

Condition: SPWM control, $V_{CC} = 800\text{V}$, $R_{g(ON)} = R_{g(OFF)} = 6.8\Omega$, $T_f = 65^\circ\text{C}$, $T_{jmax} = 175^\circ\text{C}$, PF = 0.8, Modulation rate = 1



Note3: This graph is calculated value for reference based on the limitation of $T_{jmax} = 175^\circ\text{C}$. The actual current out ability depends on inverter electrical, thermal and mechanic design. Please confirm it in actual application system.

Module

Parameter	Conditions	Value	Unit
Isolation voltage	Main terminal to base plate, f=0Hz, t=1sec	4.2	kV
Material of module baseplate	-	Cu+Ni	-
Creepage distance	terminal to heatsink terminal to terminal	9	mm
Clearance	terminal to heatsink terminal to terminal	4.5	mm
Stray inductance module	$T_f = 65^\circ\text{C}$	8	nH
Module lead resistance, terminals – chip	$T_f = 65^\circ\text{C}$	0.2	mΩ
Mounting torque for module mounting	Screw M4 baseplate to heatsink	1.8 to 2.2	Nm
Weight	-	798	g

NTC characteristics

Symbol	Parameter	Condition	Value			Unit
			Min.	Typ.	Max.	
R ₂₅	Resistance	$T_c = 25^\circ\text{C}$	-	5	-	kΩ
ΔR/R	Deviation of R ₁₀₀	$T_c = 100^\circ\text{C}$, R ₁₀₀ = 493Ω	5	-	5	%
P ₂₅	Power dissipation	$T_c = 25^\circ\text{C}$	-	-	20	mW
B _{25/50}	B-value	$R_2 = R_{25} \exp [B_{25/50}(1/T_2 - 1/(298,15 \text{ K}))]$	-	3375	-	K
B _{25/80}	B-value	$R_2 = R_{25} \exp [B_{25/80}(1/T_2 - 1/(298,15 \text{ K}))]$	-	3411	-	K
B _{25/100}	B-value	$R_2 = R_{25} \exp [B_{25/100}(1/T_2 - 1/(298,15 \text{ K}))]$	-	3433	-	K

MOSFET Electrical characteristics (T_j = 25°C unless otherwise specified, chip)

Symbol	Item	Condition		Value			Unit
				Min.	Typ.	Max	
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =6mA		1200	-	-	V
I _{DSS}	Zero gate voltage drain current	V _{DS} =1200V, V _{GS} =0V		-	-	60	μA
V _{GS(th)}	Gate-source threshold voltage	I _D =60mA, V _{DS} =V _{GS}		2.1	-	5.8	V
I _{GSS}	Gate-Source Leakage Current	V _{GS} =20V, V _{DS} =0V, T _j =25°C		-	-	10	μA
R _{DS(on)} (Chip)	Static drain-source	I _D =600A	T _j =25°C	1.5	2.2	3.1	mΩ
	On-state resistance	V _{GS} =18V	T _j =175°C	3.5	5.4	7.2	mΩ
V _{DS(on)} (Chip)	Static drain-source	I _D =600A	T _j =25°C	-	1.34	1.84	V
	On-state voltage	V _{GS} =18V	T _j =175°C	-	3.24	4.33	V
C _{iss}	Input capacitance	V _{DS} =850V		-	24	-	nF
C _{oss}	Output capacitance	V _{GS} =0V		-	1.84	-	nF
C _{rss}	Reverse transfer capacitance	f =1MHz		-	0.132	-	nF
Q _G	Total gate charge	V _{DD} =850V, I _D =600A, V _{GS} =-5/+18V		-	1140	-	nC
t _{d(on)}	Turn-on delay time	V _{DD} =600V I _D =600A V _{GS} =+15/-4V R _{G(ON)} =6.8Ω R _{G(OFF)} =6.8Ω Inductive load switching operation	T _j =25°C	-	160	-	ns
			T _j =150°C	-	140	-	
t _r	Rise time		T _j =25°C	-	125	-	ns
			T _j =150°C	-	110	-	
t _{d(off)}	Turn-off delay time		T _j =25°C	-	340	-	ns
			T _j =150°C	-	385	-	
t _f	Fall time		T _j =25°C	-	85	-	ns
			T _j =150°C	-	100	-	
E _{on}	Turn-on power dissipation		T _j =25°C	-	34.8	-	mJ
			T _j =150°C	-	28.2	-	
E _{off}	Turn-off power dissipation		T _j =25°C	-	39.6	-	mJ
			T _j =150°C	-	42	-	
R _{th(j-f)}	FET Thermal Resistance	Junction to cooling fluid ΔV/Δt =10dm³/min, T _f =65°C		-	0.104	-	K/W

Body Diode Electrical characteristics ($T_j=25^\circ\text{C}$ unless otherwise specified, chip)

Symbol	Item	Condition		Value			Unit
				Min.	Typ.	Max	
V _{SD}	Body Diode Forward Voltage	V _{GS} =-4V I _{SD} =600A	T _j =25°C	3.9	4.9	5.6	V
			T _j =175°C	3.1	4.2	5.2	
T _{rr}	Reverse recovery time	V _{DD} =600V I _D =600A	T _j =25°C	-	40	-	ns
			T _j =150°C	-	57	-	
Q _{rr}	Reverse recovery charge	V _{GS} =+15/-4V R _{G(ON)} =R _{G(OFF)} =6.8Ω	T _j =25°C	-	2.12	-	uC
			T _j =150°C	-	5.48	-	
E _{rr}	Diode switching power dissipation	Inductive load switching operation	T _j =25°C	-	0.5	-	mJ
			T _j =150°C	-	1.52	-	

Test Conditions

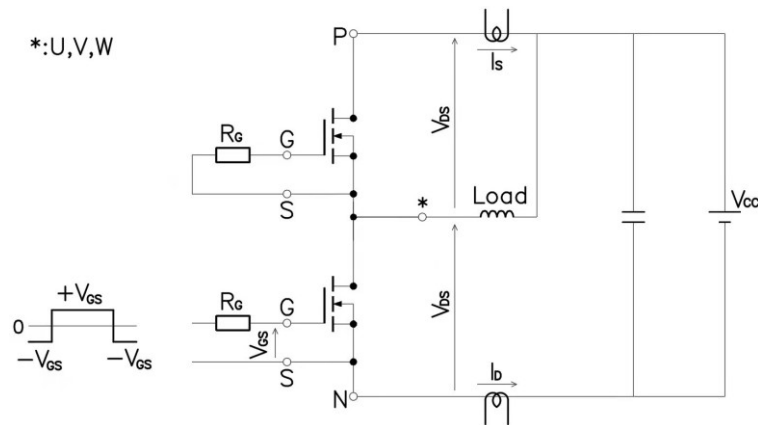


Figure 3. Switching time measure circuit

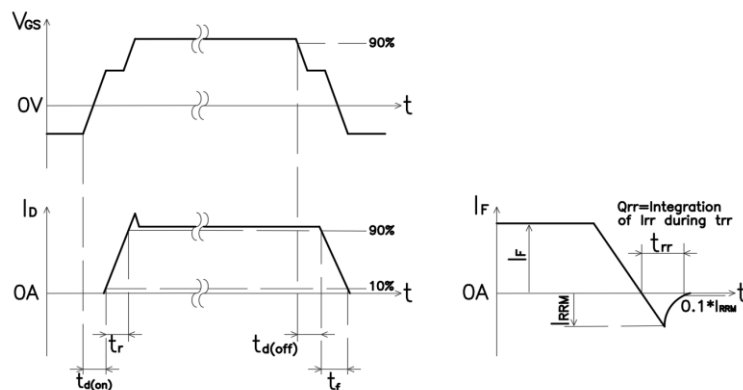


Figure 4. Switching time definition

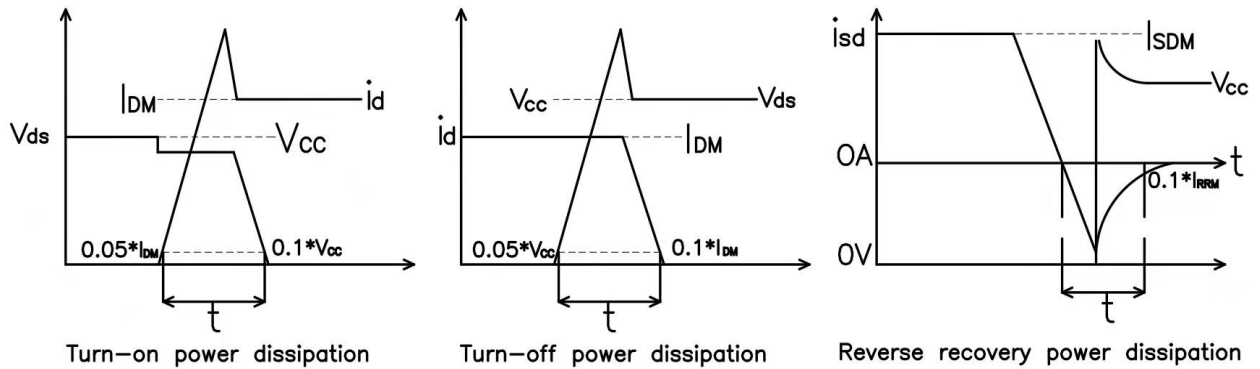


Figure 5. Switching power dissipation definition

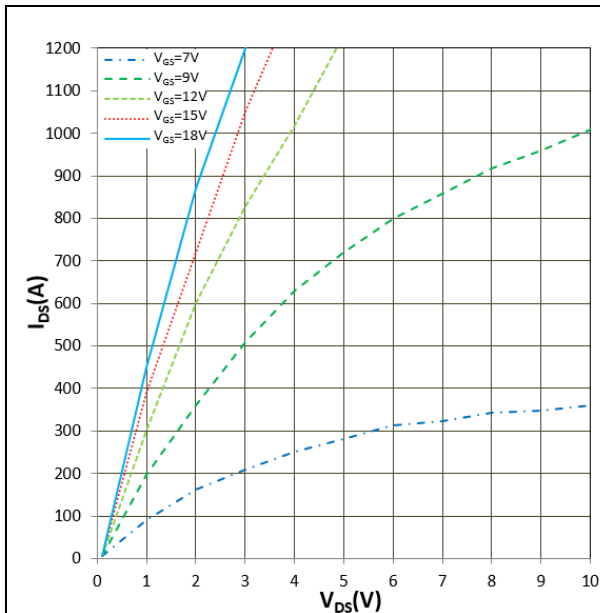


Figure 6. I_{DS} vs V_{DS}
 $T_j = 25^\circ\text{C}$, V_{GS} parameter

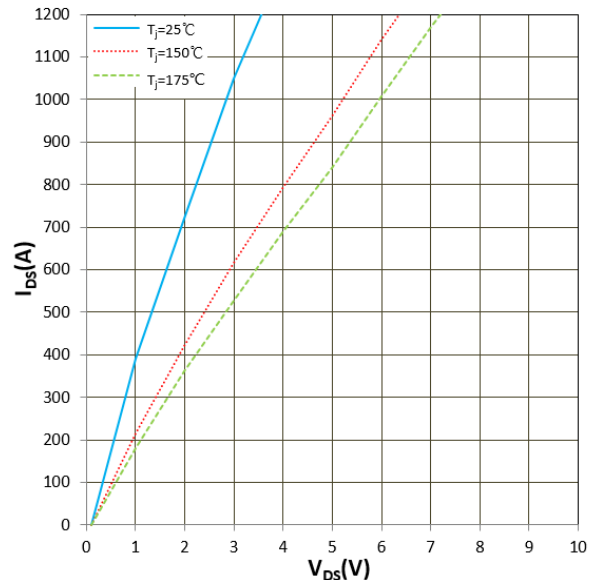


Figure 7. I_{DS} vs V_{DS}
 $V_{GS} = 15\text{V}$, T_j parameter

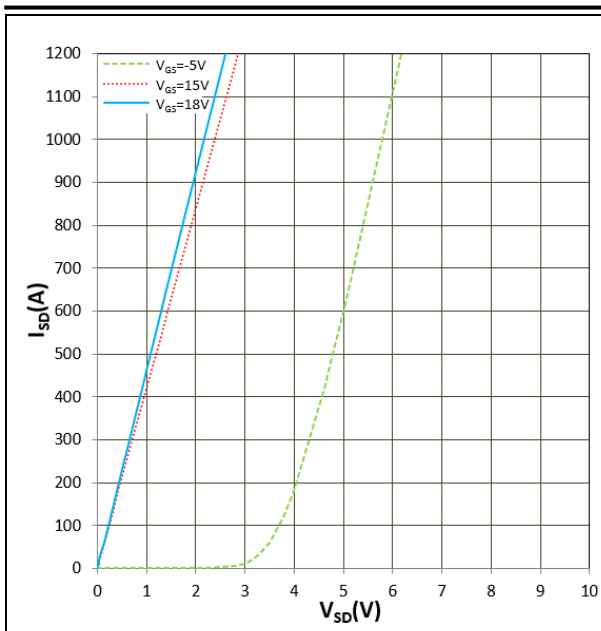


Figure 8. I_{SD} vs V_{SD}
T_j = 25°C, V_{GS} parameter

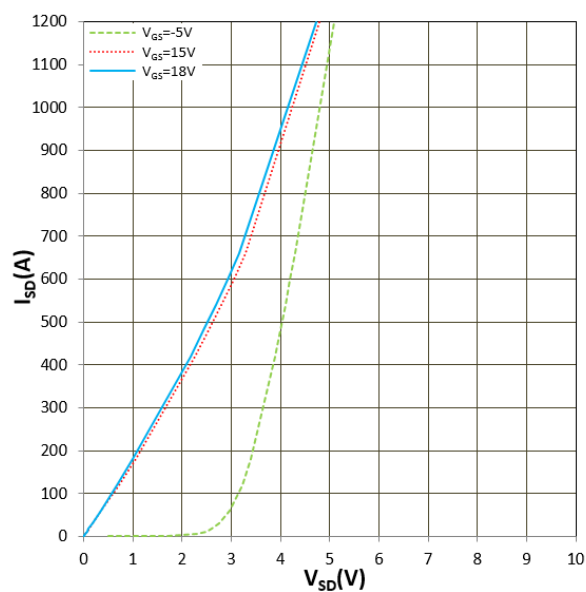


Figure 9. I_{SD} vs V_{SD}
T_j = 175°C, V_{GS} parameter

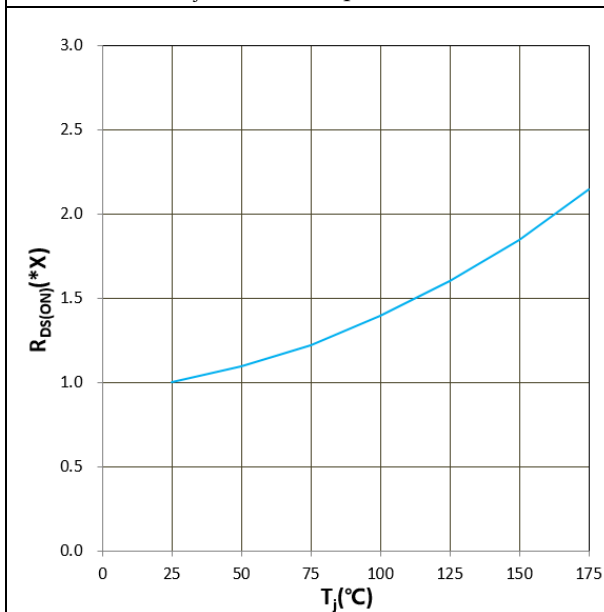


Figure 10. R_{DS(ON)} vs T_j
V_{GS} = +15V, I_D = 600A, 1.0x = 2.67mΩ

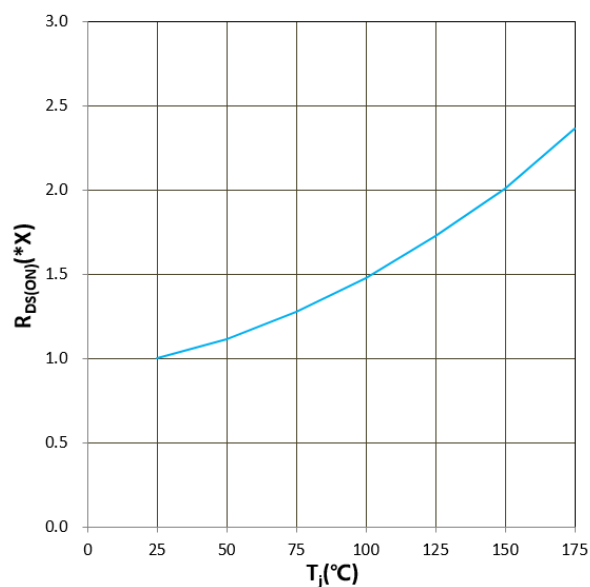


Figure 11. R_{DS(ON)} vs T_j
V_{GS} = +18V, I_D = 600A, 1.0x = 2.2mΩ

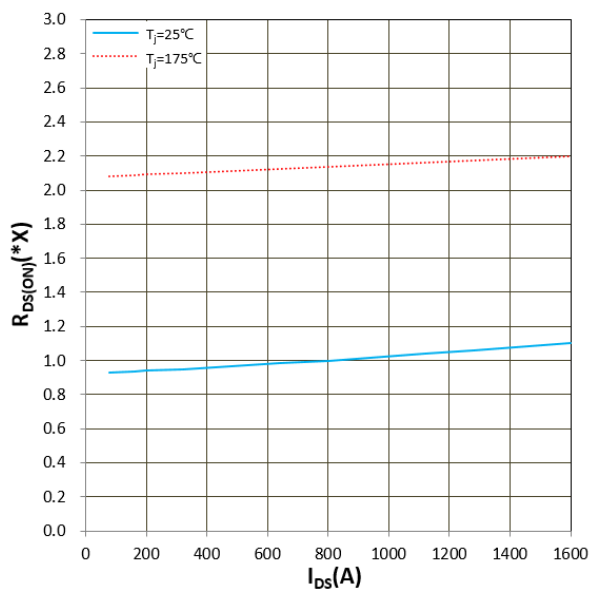


Figure 12. $R_{DS(ON)}$ vs I_{DS}
 $V_{GS} = +15V$, $1.0x = 2.67m\Omega$

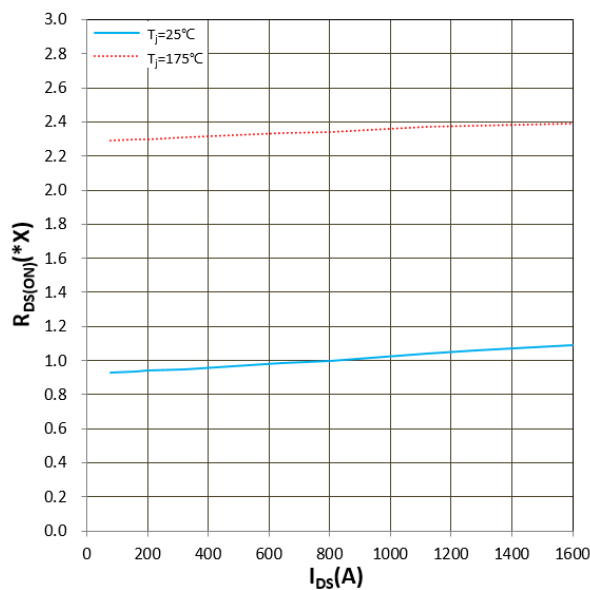


Figure 13. $R_{DS(ON)}$ vs I_{DS}
 $V_{GS} = +18V$, $1.0x = 2.2m\Omega$

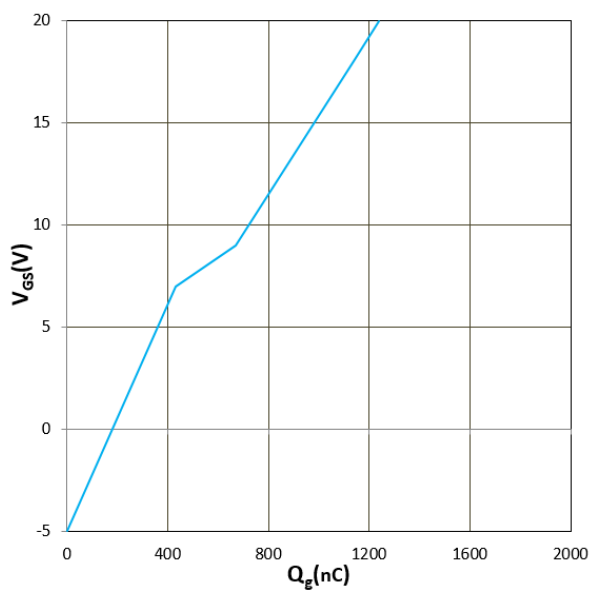


Figure 14. V_{GS} vs Q_g
 $T_j = 25^\circ C$, $I_{GS} = 6mA$

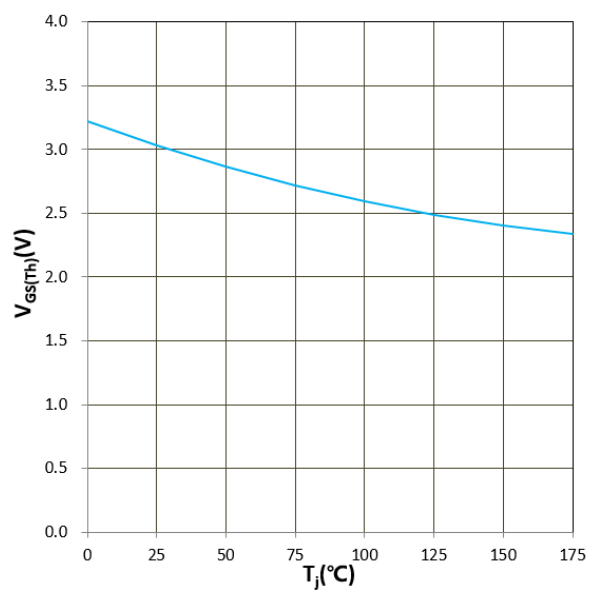


Figure 15. $V_{GS(TH)}$ vs T_j
 $V_{GS} = V_{DS}$, $I_D = 60mA$

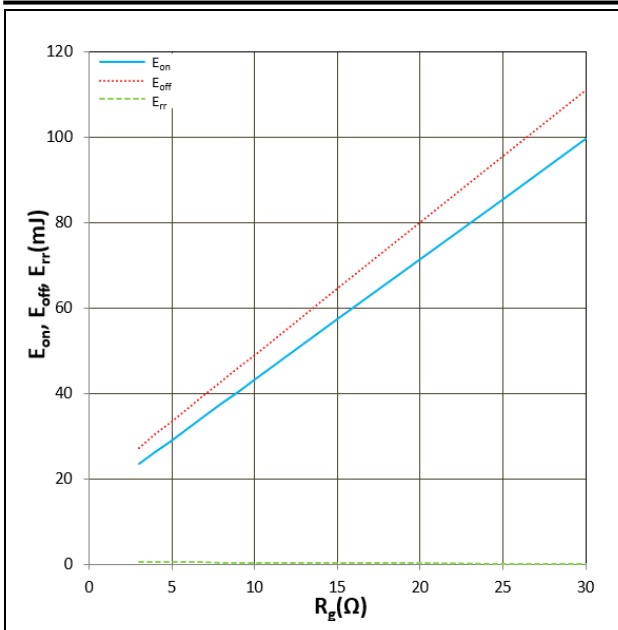


Figure 16. E_{on}, E_{off}, E_{rr} vs R_g
T_j=25°C, V_{CC}=600V, I_D=600A, V_{GS}=+15V/-4V
Inductive Load

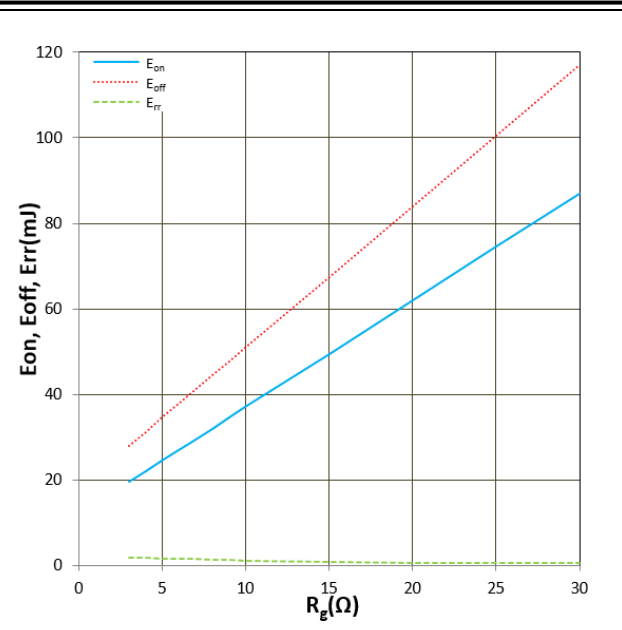


Figure 17. E_{on}, E_{off}, E_{rr} vs R_g
T_j=150°C, V_{CC}=600V, I_D=600A, V_{GS}=+15V/-4V
Inductive Load

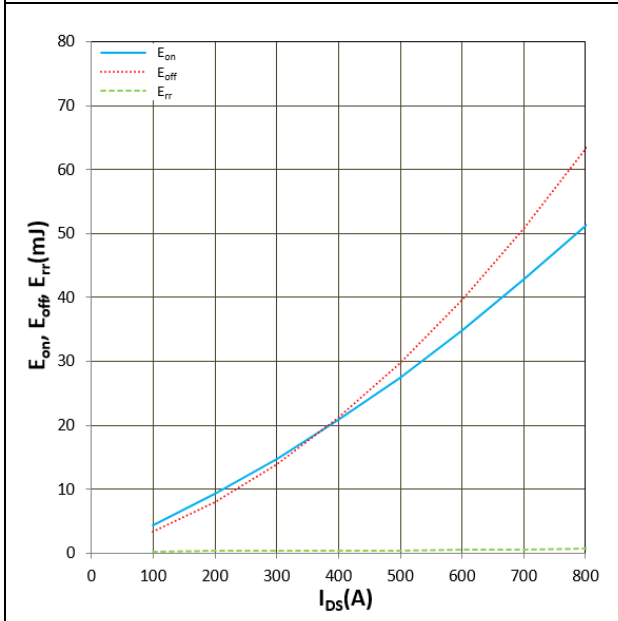


Figure 18. E_{on}, E_{off}, E_{rr} vs I_{DS}
T_j=25°C, V_{CC}=600V, R_G=6.8Ω, V_{GS}=+15V/-4V
Inductive Load

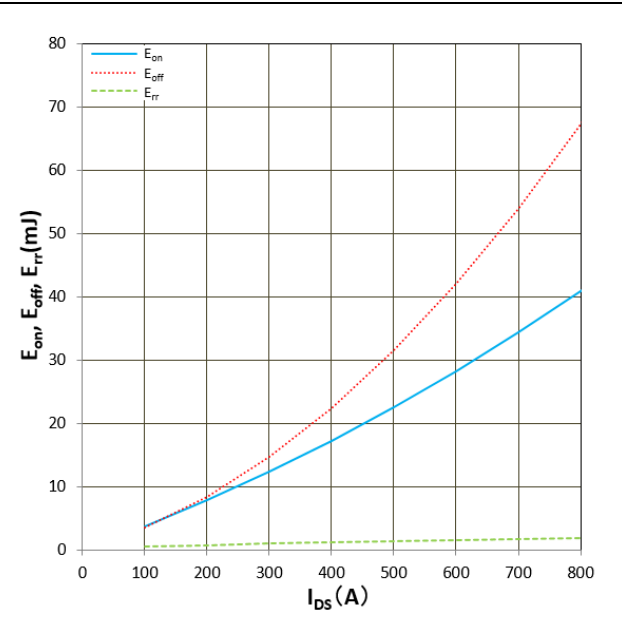


Figure 19. E_{on}, E_{off}, E_{rr} vs I_{DS}
T_j=150°C, V_{CC}=600V, R_G=6.8Ω, V_{GS}=+15V/-4V
Inductive Load

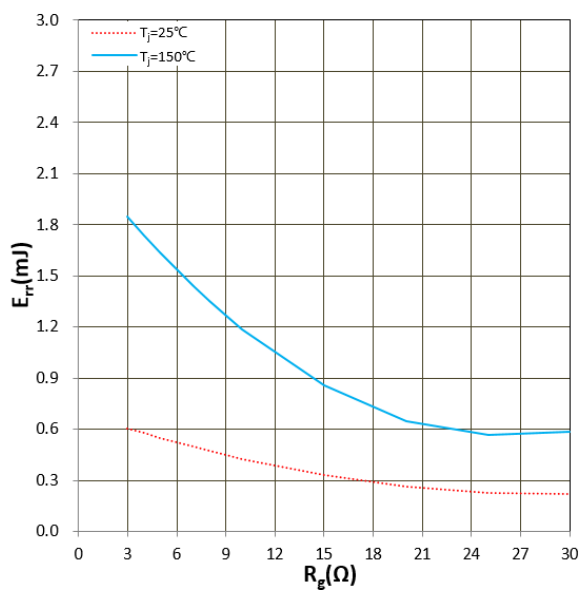


Figure 20. E_{rr} vs R_g
 $V_{DD} = 600\text{V}$, $I_F = 600\text{A}$, $V_{GS} = +15\text{V}/-4\text{V}$
 Inductive Load

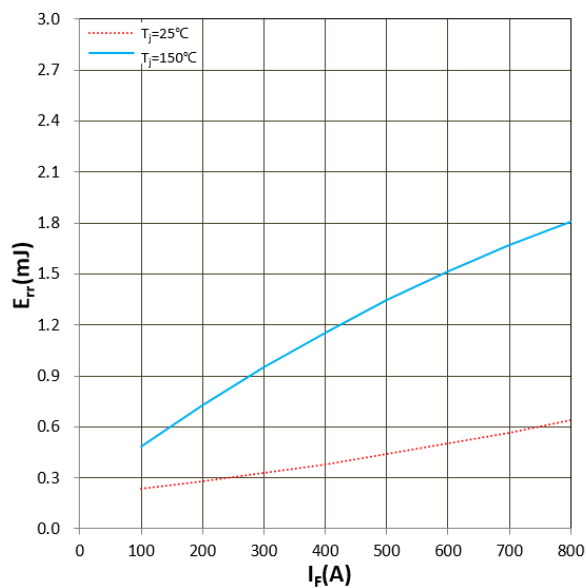


Figure 21. E_{rr} vs I_F
 $V_{DD} = 600\text{V}$, $R_g = 6.8\Omega$, $V_{GS} = +15\text{V}/-4\text{V}$
 Inductive Load

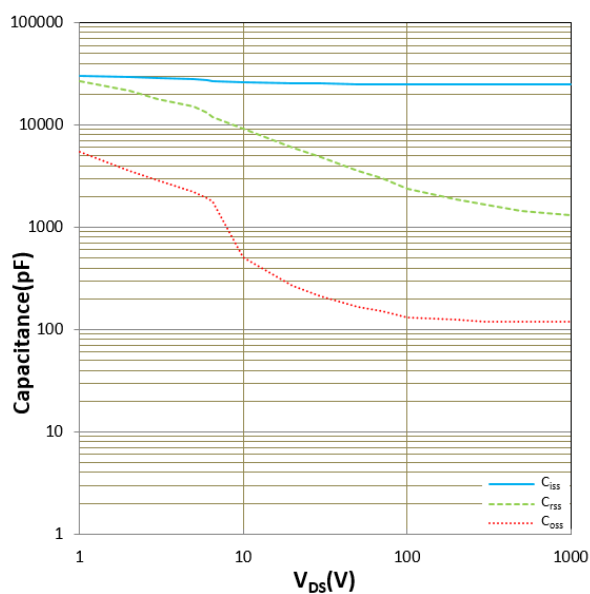


Figure 22. C_{iss} , C_{oss} , C_{rss} vs V_{DS}
 $T_j = 25^\circ\text{C}$

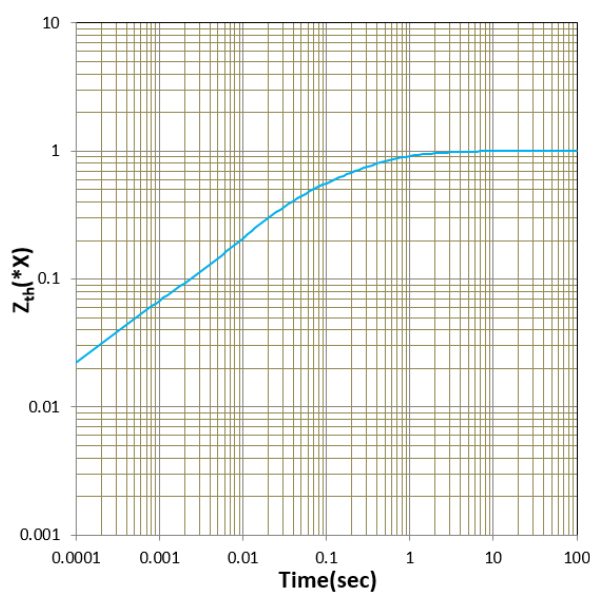


Figure 23. Transient thermal impedance
 $1.0x = 0.104\text{K/W}$

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